

31040 U.S. PTO  
09/971981  
10/04/01

J 06/572

Class	Subclass
ISSUE CLASSIFICATION	

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PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. PATENT DATE  
 SCANNED KC 103 GW Q.A.

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/971981	D	<u>361</u>	<u>257</u>	<u>2841</u>	

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TITLE  
 Electronic component overlapping a substrate with a semiconductor wafer

PTO-2040  
12/99

ISSUING CLASSIFICATION							
ORIGINAL				CROSS REFERENCE(S)			
CLASS		SUBCLASS		CLASS		SUBCLASS (ONE SUBCLASS PER BLOCK)	
INTERNATIONAL CLASSIFICATION							

Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>  <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	<b>DRAWINGS</b> Sheets Drwg.    Figs. Drwg.    Print Fig.			<b>CLAIMS ALLOWED</b> Total Claims    Print Claim for O.G.	
	_____ (Assistant Examiner)    _____ (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
	_____ (Primary Examiner)    _____ (Date)			<b>ISSUE FEE</b> Amount Due    Date Paid	
_____ (Legal Instruments Examiner)    _____ (Date)			<b>ISSUE BATCH NUMBER</b>		

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